

"Lucian Blaga" University of Sibiu Computer Science and Electrical Engineering Department



PhD Advisors: Prof. Remus BRAD, Prof. Adrian FLOREA



- About me & where I come from
- Introduction
- Methodology
- Current state
  - Attacks
  - Countermeasures
- Research ideas
- Next steps



## ROMANIA

- Location: Eastern Europe
- Capital: Bucharest
- Population: 19 mil (2022)
- Neighbors:
  - Ukraine (N)
  - Hungary (W)
  - Serbia (S-W)
  - Bulgaria (S)
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- It has mountains, hills, plains, opening to the sea, and a delta

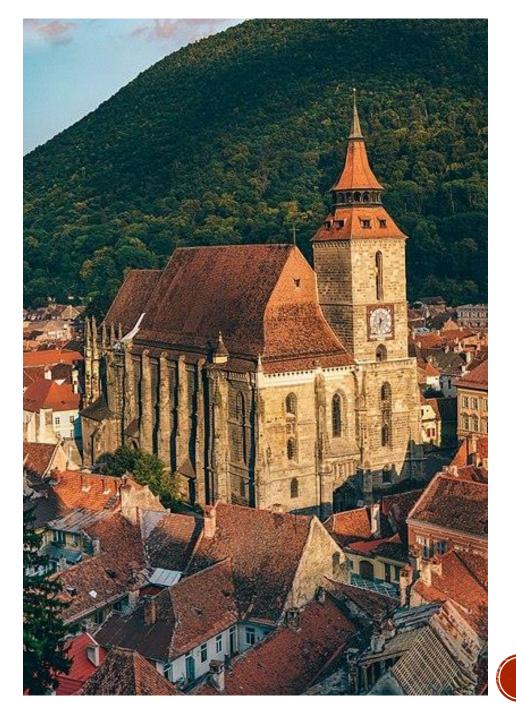




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- Transfăgărăşan Road the 'world's best driving road' (declared by Top Gear).
  - Over 151 kilometres (93 miles)
  - Cutting through the Făgăraş Mountains
  - The top is at 2,134 meters (7,000 feet).







- The Merry Cemetery on the crosses the deceased's message for the living world
  - Under this heavy cross, lies my poor mother-inlaw, if she was alive another three days, I would have been lying here instead of her. You who pass by here try not to wake her up, because if she comes home, she will scold me again. Stay here, my dear mother-in-law.



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- Francesco Illy the creator of the coffee machine was born in Timisoara





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- The 7<sup>th</sup> fastest internet speed in the world with a peak of 58.7 Mbps

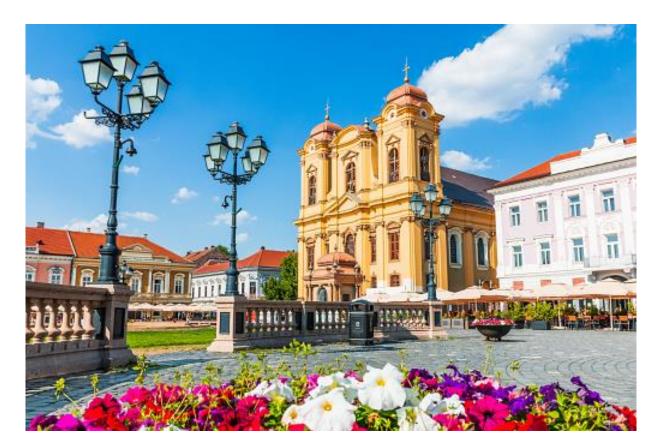




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- Bran Castle known as castle of Dracula, but Count Vlad Tepes (which inspired the Dracula character) didn't actually live there



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- European capital of culture in 2007, together with Luxembourg
- "ASTRA" National Museum of Ethnography, Civilization and Folk Arts – 3 Michelin stars



#### ABOUT ME

- Practiced Speed Skating during Primary School and High School (~10 years)
- Like to read and travel
- BSc Computer Science, Faculty of Engineering, ULBS (2014-2018)
- MSc Advanced Computing Systems, Faculty of Engineering, ULBS (2018-2020)
- PhD Candidate Security Issues on Computer Architecture, ULBS (2020-present)
- Software Validation Engineer on Network Devices at <u>(Internal)</u> (2016-present)

- Teaching Assistant (Labs, Projects, Grading) – prof. Adrian FLOREA
  - Simulation and Optimization of Computing Architectures (different types of caches and predictors)
  - Embedded Systems (VEX simulator -Linux)
  - Microprocessors Systems (MIPS & DLX Assembly)





**Prof Lucian VINTAN** Dynamic Neural Branch Prediction (IJCNN '99, Washington DC)

 PhD Coordinator – prof. Remus BRAD





# FACULTY OF ENGINEERING & PHD PROGRAMS





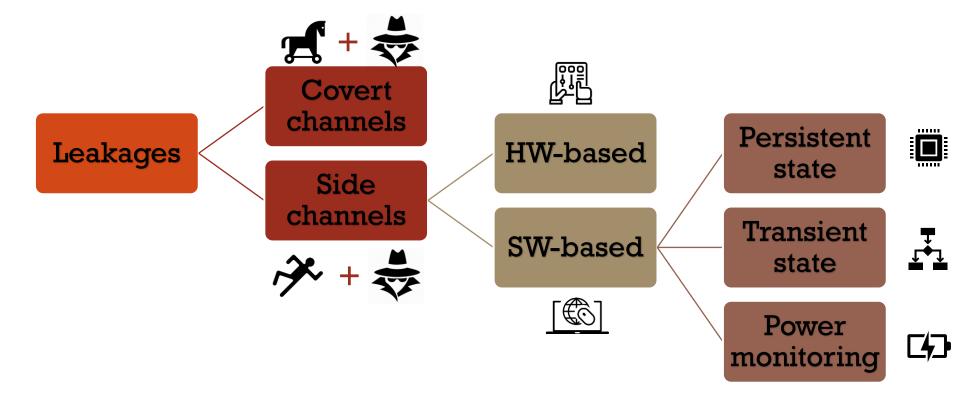


Financed by the state budget – you don't pay anything Tax - you need to pay ~ 1500 € / year



#### **INTRODUCTION**

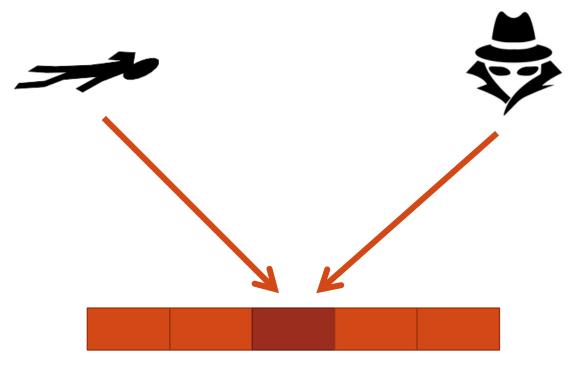
 The movement to the virtual space, apart from the huge benefits, comes with the unwanted threats too





#### **METHODOLOGY**

 The attack techniques run in spy processes, and they target the cryptographic algorithms which are running as victim processes.



Cryptographic algorithms:

AES

RSA

ECDSA

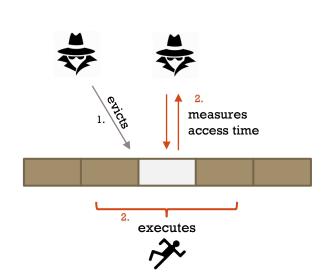
ElGamal

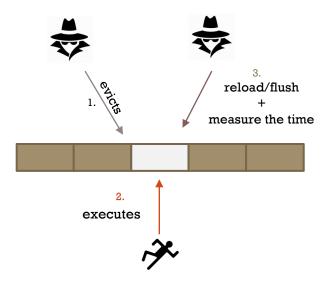


#### PERSISTENT STATE CHANNELS - CACHE

 Timing-based attacks - the attacker is analyzing the time taken to execute cryptographic algorithm

> Evict&Time



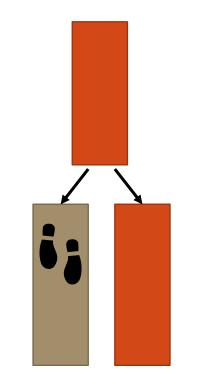


- Access-based attacks the attacker is monitoring the accesses done by the victim to execute cryptographic algorithm
  - Flush&Reload
  - Flush&Flush
  - Evict&Relaod
  - Prime&Probe
  - Prime&Abort



#### TRANSIENT STATE CHANNELS

- Appear due to the high parallelism in the modern CPUs which execute instructions in an **out-of-order** way, or before their turn in the program.
- The predictor can be trained to misspsculate
- The microarchitectural state won't be cleared of data in case of a wrong speculation.
- A series of very well known "speculation" attacks are Spectre, Meltdown and their variations.

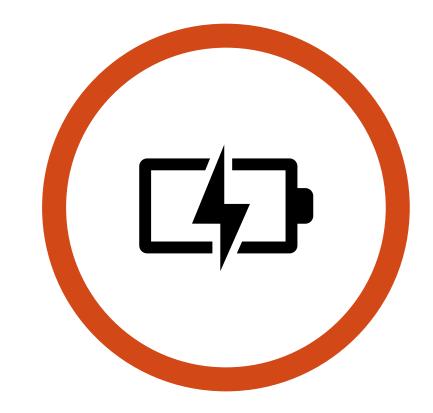


```
if(x < arrayLength)
{
    i = array[x];
    y = array2[i*256];
}</pre>
```



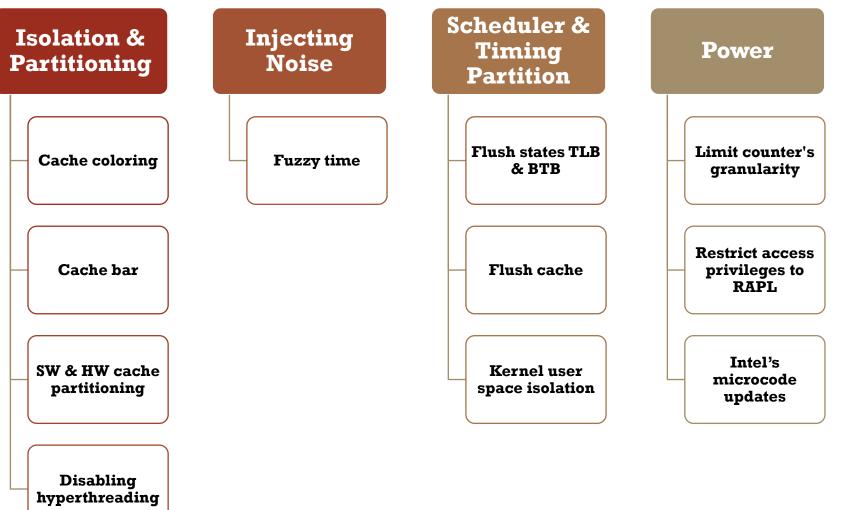
#### POWER MONITORING

- The power side-channel attacks analyze the variations of the power consumption of a device to extract secret data, without physical accessing the targeted system.
- Platypus attacks use the Intel RAPL system to gain information about the power consumption of the system and based on some analysis each kind of instruction can be distinguished.
- ThermalBleed is using the *hwmon* interface in Linux that can read the temperature sensor for each core to **break the KASLR** (Kernel Address Space Layout Randomization)





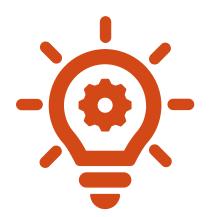
#### **DETECTION & COUNTERMEASURES**



Current State

Security issues in computer architecture – research project

#### **RESEARCH IDEAS**



- Combine the idea from Persistent State channels with Power monitoring channels
- In ThermalBleed they didn't explore the cache, using the following affirmation:
  - Thermal difference appears when the cache is accessed (higher temp) and when the main memory is accessed (lower temp)
- Same as in Flush+Reload attack, instead of reading the time of the reloading data in cache after the victim executes, we can measure the temperature to see if there was a cache hit or not

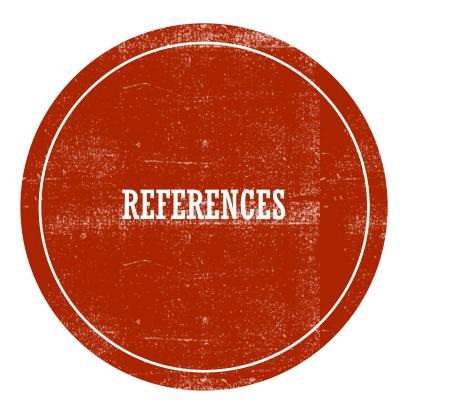




#### NEXT STEPS/OBJECTIVES

- The collecting app which measures the temperature done
- The target app which makes the encryptions (RSA algorithm) - in work
- Distinguish between cache and main memory accesses -> reproduce
- Apply the distinguish technique on the encryptions
- Recover the encryption key





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- M. Lipp, A. Kogler, et al. "PLATYPUS: Software-based Power Side-Channel Attacks on x86" in IEEE Symposium on Security and Privacy, San Francisco, CA, 2021.
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